REMARKS

The title has been amended to conform with the translated title of the published application (WO 00/26767).

The specification has been amended to include a reference to the priority applications.

The claims have been amended to remove reference indicia.

To meet the requirements of the United States, the Abstract (as originally filed in the PCT application) is added.

No fee is believed to have been incurred by virtue of this amendment. However if a fee is incurred on the basis of this amendment, please charge such fee against deposit account 07-0832

Respectfully submitted,
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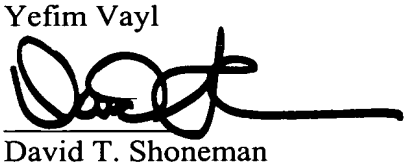

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MARKED UP VERSION OF THE AMENDED SPECIFICATION

Please amend the specification as follows. A marked up version of the amended specification is attached herewith:

Page 1, lines 1-2, delete "METHOD AND APPARATUS FOR UPDATING COMPUTER CODE USING A INTEGRATED CIRCUIT INTERFACE" and insert -- METHOD AND APPARATUS FOR UPDATING COMPUTER CODE USING AN INTEGRATED CIRCUIT INTERFACE --

On Page 1 amend the first paragraph as follows:

-- This application claims the benefit of U.S. provisional application serial no. 60/106,809 filed November 3, 1998, which is hereby incorporated herein by reference, and which claims the benefit under 35 U.S.C. § 365 of International Application PCT/US99/25253, filed November 3, 1999, which was published in accordance with PCT Article 21(2) on May 11, 2000 in English.--

MARKED UP VERSION OF THE AMENDED CLAIMS

Please amend the claims (which are the annexes of the International Preliminary Examination Report) as follows. A marked up version of the amended claims is attached herewith.

1.(AMENDED) An apparatus [(100)] for loading computer code comprising:

a card interface [(120)] capable of distinguishing between a conventional integrated circuit card and a memory card [(104)];

a memory card [(104)] comprising a memory unit [(114)] and a memory unit controller [(116)]; and

a computer controlled device memory unit [(110)] for storing a first computer code [(124)] that is downloaded from the memory unit [(114)] of the memory card [(104)].

2.(AMENDED) The apparatus of claim 1 wherein a second computer code [(122)] stored in the computer controlled device memory unit is updated by the first computer code [(124)] stored in the memory unit [(114)] of the memory card [(104)].

3.(AMENDED) The apparatus of claim 1 wherein said memory card [(104)] comprises at least one high speed data port [(128)].

4.(AMENDED) The apparatus of claim 3 wherein the at least one high speed data port [(128)] is used to transmit the first computer code [(124)] from the memory card memory unit [(114)] to the computer controlled device memory unit [(110)].

5.(AMENDED) The apparatus of claim 1 wherein said card interface comprises:

means for producing a first signal [(208)] that is coupled to an integrated circuit card connection [(118)]; and

means for analyzing a second signal that is produced by a memory card in response to said first signal [(210)].

6. The apparatus of claim 5 wherein said second signal is not produced by integrated circuit cards that are not memory cards.

7.(AMENDED) The apparatus of claim 5 wherein said card interface [(120)] applies said first signal to a clock signal connector of said integrated circuit card connection [(118)] and receives said second signal on a data input/output signal connector of said integrated circuit card connection [(118)].

8.(AMENDED) The apparatus of claim 1 wherein said card interface [(120)] further comprises at least one high speed data path [(128)] with said memory card [(104)].

9.(AMENDED) The apparatus of claim 1 wherein said card interface [(120)] further comprises :

means for transferring computer code from said memory card to said computer controlled device memory unit [(108)].

10.(AMENDED) The apparatus of claim 1 wherein said card interface [(120)] further comprises:

means for accepting or rejecting the computer code for transference from said memory card to said computer controlled device memory unit [(218)].

11.(AMENDED) A method of loading computer code in a computer controlled device comprising the steps of:

identifying whether an integrated circuit card is a memory card or a conventional integrated circuit card [(212)]; and,

transferring the computer code through a high speed data port of a memory card into said computer controlled device [(222)].

12.(AMENDED) The method of claim 11 wherein said identifying step further comprises the steps of:

applying a first signal to said memory card [(208)]; and

analyzing a second signal produced by said memory card in response to said first signal to determine if said integrated circuit card is a memory card [(210)].

13.(AMENDED) The method of claim 12 wherein said transferring step further comprises:

activating an NRSS interface [(216)].